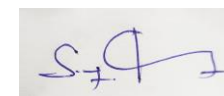


K.L.N. COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
CENTRALIZED INTERNAL TEST SCHEDULE – 2019– 2020 (EVEN SEMESTER)

Ref: KLNCE/EEE/CIT/JAN'2020

Date: 13.01.2020

SEMESTER	SUBJECT CODE & NAME OF THE SUBJECT					
IV	MA8491 Numerical Methods	EE8401 Electrical Machines – II	EE8402 Transmission and Distribution	EE8451 Linear Integrated Circuits and Applications	IC8451 Control Systems	EE8403 Measurements and Instrumentation
VI	EE8601 Solid State Drives	EE8602 Protection and Switchgear	EE8691 Embedded Systems	EE8002 Design of Electrical Apparatus (Professional Elective I)	EE8005 Special Electrical Machines (Professional Elective II)	-
VIII	EE6801 Electric Energy Generation, Utilization and Conservation	EE6009 Power Electronics for Renewable Energy Systems (Elective IV)	GE6757 Total Quality Management (Elective V)	-	-	-
DATE OF TEST						
Test-I-CIT – I (09:10am – 10:40 am)	27.01.2020	28.01.2020	29.01.2020	30.01.2020	31.01.2020	01.02.2020
Test-II-CIT – II (09:10am – 10:40 am)	20.02.2020	21.02.2020	22.02.2020	24.02.2020	25.02.2020	26.02.2020
Test-III- CIT-III (Model Theory Examination)	21.03.2020	23.03.2020	24.03.2020	26.03.2020	27.03.2020	28.03.2020
Last Date for submission						
	CIT-I		CIT – II		CIT-III -Model Theory	
Question paper to dept.(for copier print out)	21.01.2020		13.02.2020		14.03.2020	
Question paper with key to EMC	25.01.2020		19.02.2020		20.03.2020	
Automation Entry	With in 3 working days after the end of test of the subject concerned					
Submission of Result Analysis for CIT	07.02.2020		02.03.2020		02.04.2020	



HOD / EEE

Cc to Convener/EMC, Cc to staff and student notice board, Cc to file, Cc to HOD/Math's.